



**MILITARY DATA SHEET**

**MNLM120-12-K REV 0BL**

Original Creation Date: 07/06/95  
Last Update Date: 12/10/96  
Last Major Revision Date: 07/06/95

**THREE TERMINAL NEGATIVE REGULATOR**

**Industry Part Number**

LM120K-12

**NS Part Numbers**

LM120K-12/883

**Prime Die**

LMJ120EG-12

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**Processing**

MIL-STD-883, Method 5004

**Quality Conformance Inspection**

MIL-STD-883, Method 5005

**Subgrp Description**

**Temp ( °C)**

1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55

## Electrical Characteristics

### DC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.)  
DC:  $I_L = 5\text{mA}$

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN-NAME	MIN	MAX	UNIT	SUB-GROUPS
Iq	Quiescent Current	Vin = -14V	3			4	mA	1, 2, 3
		Vin = -32V	3			4	mA	1, 2, 3
Delta Iq	Quiescent Current Change	Vin = -17V, $5\text{mA} \leq I_L \leq 1\text{A}$	3			0.4	mA	1
		Vin = -17V, $5\text{mA} \leq I_L \leq 1\text{A}$	3			0.5	mA	2, 3
		$-32\text{V} \leq \text{Vin} \leq -14\text{V}$	3			0.4	mA	1
		$-32\text{V} \leq \text{Vin} \leq -14\text{V}$	3			0.5	mA	2, 3
Rload	Load Regulation	Vin = -17V, $5\text{mA} \leq I_L \leq 1\text{A}$	3		-80	80	mV	1, 2, 3
Rline	Line Regulation	$-32\text{V} \leq \text{Vin} \leq -14\text{V}$	3		-10	10	mV	1
		$-32\text{V} \leq \text{Vin} \leq -14\text{V}$	3		-20	20	mV	2, 3
Ios	Short Circuit Current	Vin = -32V	1, 3		0.4	3	A	1
Vout	Output Voltage	Vin = -17V	3		-12.3	-11.7	V	1
		Vin = -32V	3		-12.5	-11.5	V	1, 2, 3
		Vin = -32V, $I_L = 1\text{A}$	3		-12.5	-11.5	V	1, 2, 3
		Vin = -14.5V	3		-12.5	-11.5	V	1, 2, 3
		Vin = -14.5V, $I_L = 1\text{A}$	3		-12.5	-11.5	V	1, 2, 3
Rr	Ripple Rejection	f = 120Hz	2, 3		56		dB	4

### DC PARAMETERS: DRIFT VALUES

(The following conditions apply to all the following parameters, unless otherwise specified.)  
DC:  $I_L = 5\text{mA}$ . "Deltas not required on B-Level product. Deltas required for S-Level product ONLY as specified on Internal Processing Instructions (IPI)."

Iq	Quiescent Current	Vin = -14V	3		-0.4	0.4	mA	1
Vout	Output Voltage	Vin = -14.5V	3		-0.12	0.12	V	1

Note 1: Datalog will have negative readings.  
Note 2: Bench test, use 70256656.  
Note 3: Pre Burn-In Stress Test per (SG)RPI-3-371.

### Graphics and Diagrams

GRAPHICS#	DESCRIPTION
09107HR	(blank)
K02CRB	(blank)

See attached graphics following this page.